IN THE CLAIMS

Presented below is a complete listing of claims in the revised format set forth by the Office on 01/31/03.

1. (Currently amended) A method, comprising:

providing a first resistor with a first end and a second end, said first end coupled to a switch and said second end coupled to a data bus wire at a near end of a data bus;

controlling said switch with a detach control signal sent from a far end of said data bus to cause an apparatus containing said first resistor and said switch to enter a logically detached state; and

switching a biasing voltage from said resistor utilizing said switch.

~~2. (Cancelled)~~

3. (Original) The method of claim 1, wherein said first resistor is configured as a pull-up resistor.

4. (Original) The method of claim 3, further comprising detecting said switching of said biasing voltage.

5. (Original) The method of claim 4, further comprising determining a logically detached state responsive to said detecting.

6. (Original) The method of claim 1, wherein said detach control signal is responsive to a wake-up signal.

7. (Original) The method of claim 6, wherein said detach control signal is asserted when said wake-up signal is de-asserted.

8. (Currently amended) An apparatus, comprising:
a first resistor with a first end and a second end;
a switch coupled to said first end of said first resistor and to a bias voltage;
a detach control signal wire of a data bus coupled to said switch at a near end of said data bus, to receive a detach control signal sent from a far end of said data bus to cause said apparatus to enter a logically detached state; and
a data bus wire of said data bus coupled to said second end of said first resistor.

9. (Previously amended) The apparatus of claim 8, wherein said switch may apply said bias voltage to said first end of said first resistor responsively to said detach control signal on said detach control signal wire.

10. (Original) The apparatus of claim 9, wherein said detach control signal is generated responsively to a wake-up signal.

11. (Previously amended) The apparatus of claim 8, wherein said data bus carries universal serial bus data.

12. (Previously amended) The apparatus of claim 8, wherein said data bus carries IEEE-1394 bus data.

13. (Original) The apparatus of claim 8, further comprising a second resistor with a first end and a second end, said first end coupled to said data bus wire.

14. (Previously amended) The apparatus of claim 13, wherein said second end of said second resistor is coupled to signal ground.

15. (Previously amended) An apparatus, comprising:

means for providing a first resistor with a first end and a second end, said first end coupled to a switch and said second end coupled to a data bus wire at a near end of a data bus;

means for controlling said switch with a detach control signal sent from a far end of said data bus to cause said apparatus to enter a logically detached state; and

means for switching a biasing voltage from said resistor utilizing said switch.

~~16. (Cancelled)~~

17. (Previously amended) The apparatus of claim 15, further comprising

means for detecting said switching of said biasing voltage.

18. (Previously amended) The apparatus of claim 15, wherein said detach control signal is responsive to a wake-up signal.

19. (Previously added) A system, comprising:

a data bus with a near end and a far end;

a first circuit, coupled to said near end, including a first resistor with a first end and a second end, a switch coupled to said first end of said first resistor and to a bias voltage, a data bus wire of said data bus coupled to said second end of said first resistor, a detach control signal wire of said data bus coupled to said switch to receive a detach control signal; and

a second circuit, coupled to said far end, to send said detach control signal to cause said first circuit to enter a logically detached state.

20. (Previously added) The system of claim 19, wherein said switch may apply said bias voltage to said first end of said first resistor responsively to said detach control signal.

21. (Previously added) The system of claim 20, wherein said detach control signal is sent in response to a wake-up signal.

22. (Previously added) The system of claim 21, wherein said wake-up signal is sent by said first circuit.